

Sub-Wavelength Analog Optical Delay Lines

J.H. Abeles, R.D. Whaley, Jr., A. Lepore, M. Kwakernaak, V. Khalfin

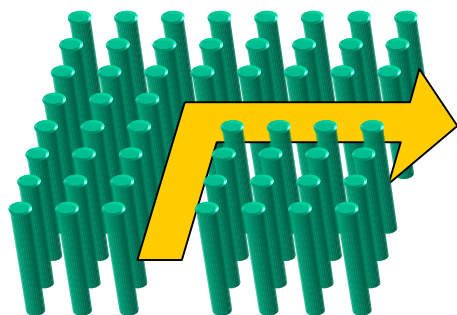
jabeles@sarnoff.com, (609) 734-2571

Sarnoff Corporation, 201 Washington Rd, Princeton, NJ 08543

DARPA/MTO Analog Optical Signal Processing Kick-Off Meeting

Sheraton San Diego Hotel and Marina
San Diego, California

August 7, 2002



The SWAODL* Approach

- Use of low-loss InP-based serpentine waveguide to give:
 - delays on order of 3 ps \rightarrow 10 nsec
 - path lengths of 100 cm \rightarrow 5 m on 1 cm² chip size
- “Programmable” taps utilizing resonant InP-microring resonators, tuned by bias.
- Development of ultra-low loss InP waveguides ($\alpha < 0.1 \text{ cm}^{-1}$) and tight-bend radius structures ($r < 10 \text{ }\mu\text{m}$).

**Sub-Wavelength Analog Optical Delay Lines*

APPROVED FOR PUBLIC RELEASE – DISTRIBUTION UNLIMITED

Sarnoff Sub-Wavelength Analog Optical Delay Lines



SWAODL Conceptual Overview

Need: $Q > 10^6$ as demanded by RF channelization.

Approach: Low-loss resonantly coupled InP microring resonators.

Need: Time delay of 10 nsec (~ 3 meter aperture).

Approach: InP-based serpentine waveguide structure with $r < 10 \mu\text{m}$.

Need: Semiconductor propagation loss $< 0.1 \text{ cm}^{-1}$.

Approach: Low material overlap waveguide structures (ridge and/or photonic "airguide").

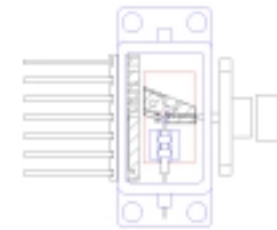


MTO AOSP

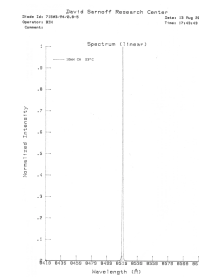
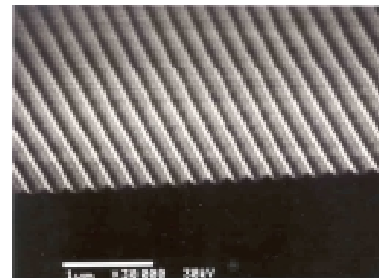
Sarnoff Photonics: Examples

Low-Capacitance Gain Element

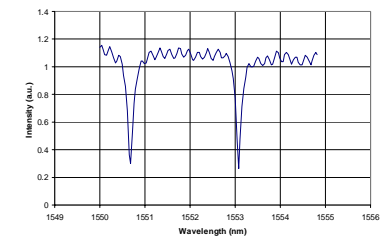
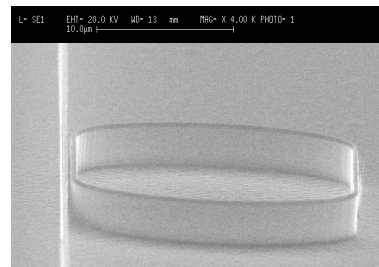
High-speed device packaging



Narrow linewidth DFBs

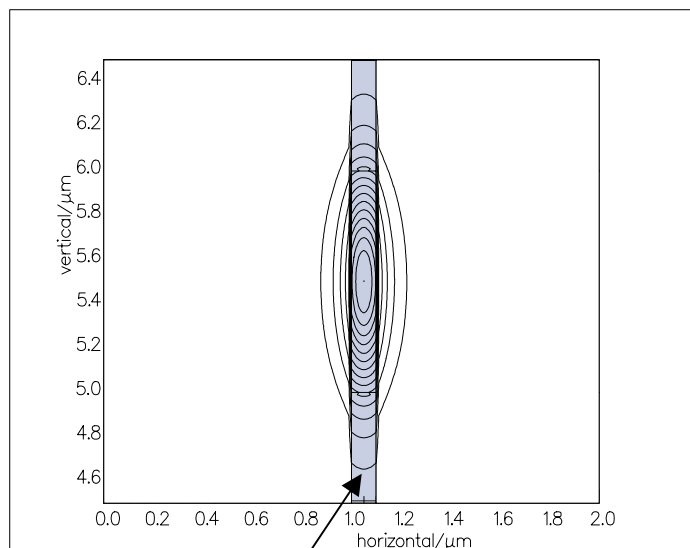


Resonant Modulators

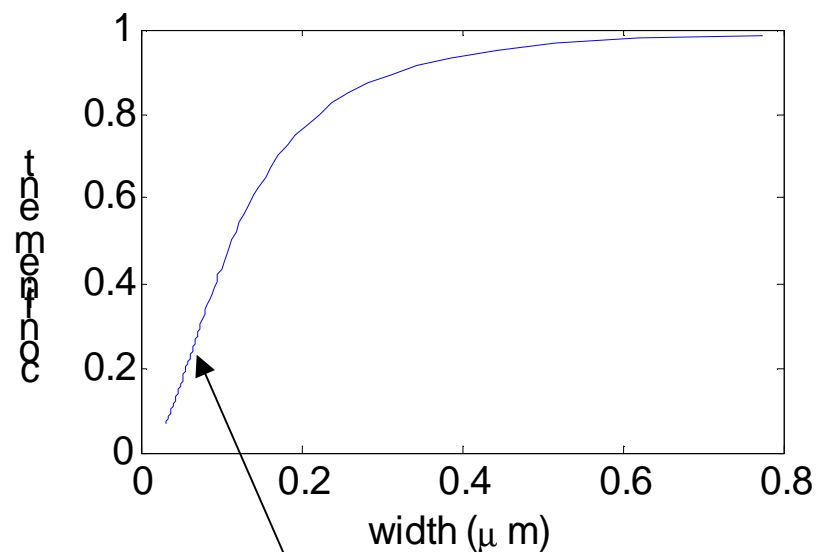


Development of Low-loss Waveguides Architectural Considerations

Single deeply-etched InP/InGaAsP rib waveguide



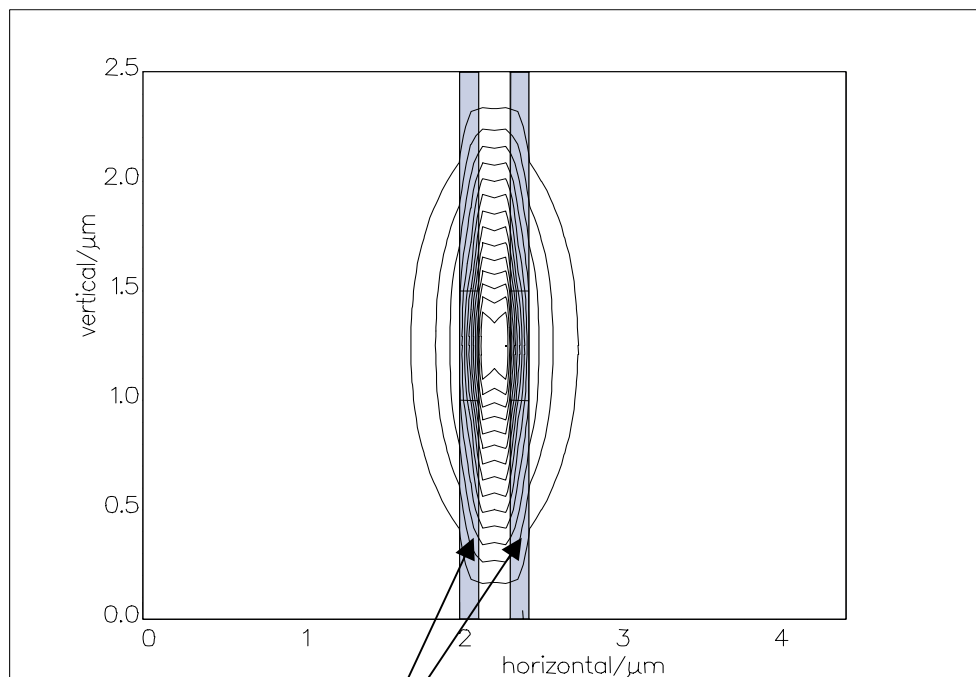
Use of narrow ridge increases percentage of modal propagation outside of lossy InP material ($\alpha \sim 1 \text{ cm}^{-1}$)



Decrease of ridge width to 0.1 to 0.2 μm range decreases loss to approximately 0.1 cm^{-1} .

Development of Low-loss Waveguides Architectural Considerations (2)

Twin deeply-etched InP/InGaAsP rib waveguides

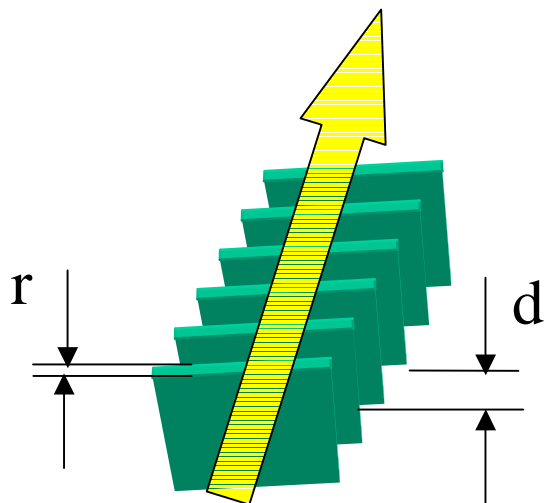


TM mode of twin waveguide structure with rib width of $0.12\ \mu\text{m}$, gap of $0.2\ \mu\text{m}$, and overlap of 0.25. Estimated loss would be $0.1\ \text{cm}^{-1}$.

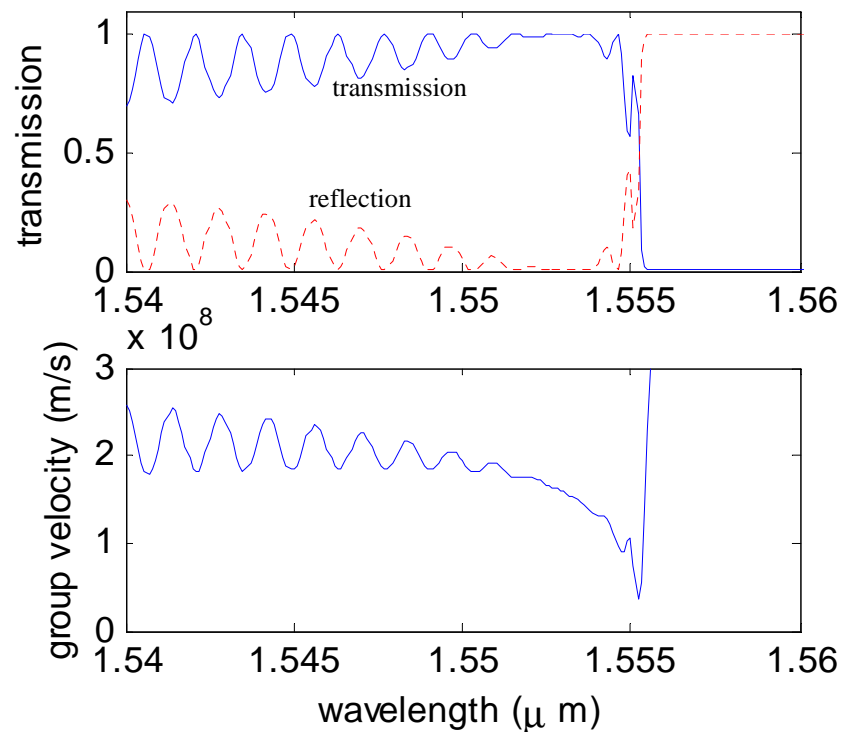
Use of twin narrow ridges confines majority of mode to outside of InP ribs.

Development of Low-loss Waveguides Architectural Considerations (3)

1-D photonic crystal slab rib waveguide



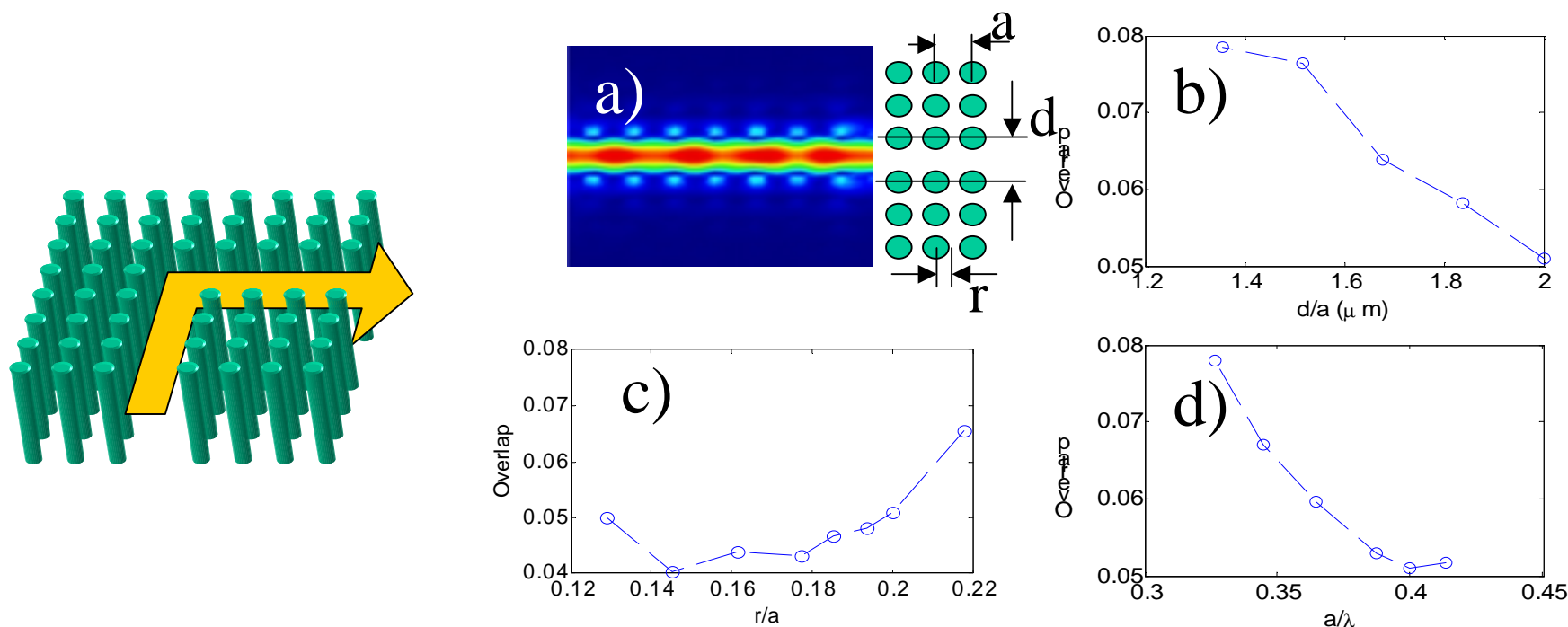
Further reduction of material overlap by forming 1-D photonic crystal structure.



For $\lambda=1.552$ to $1.553 \mu\text{m}$, losses are reduced to $<0.1 \text{ cm}^{-1}$.

Development of Low-loss Waveguides Architectural Considerations (4)

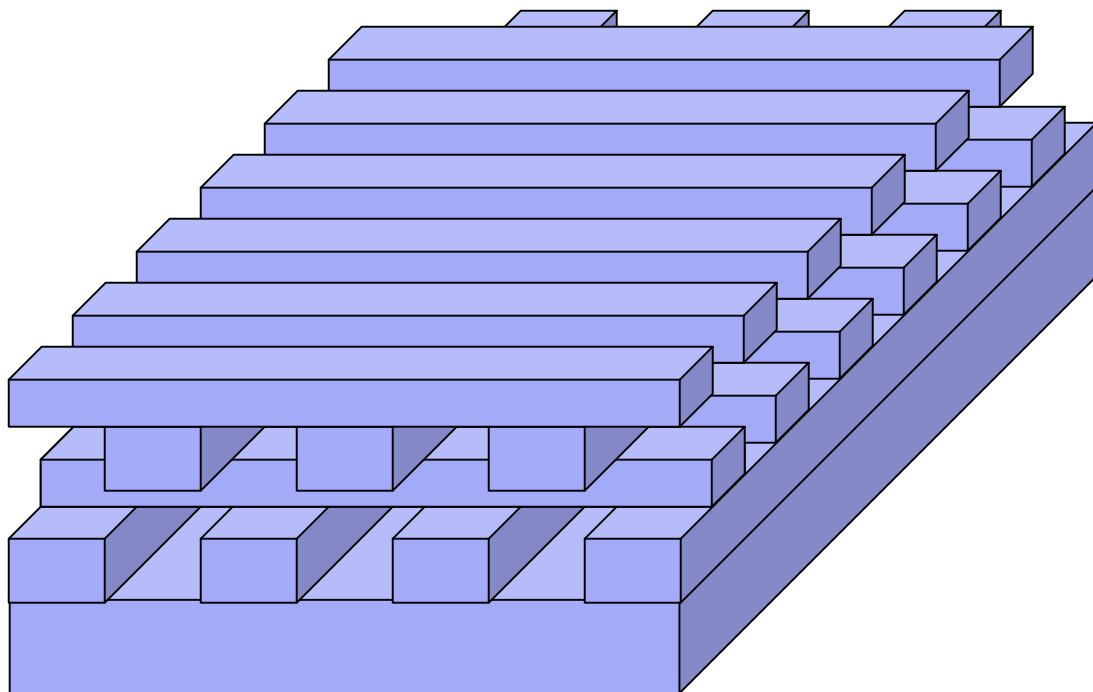
2-D photonic crystal “airguide”



Ability to achieve extremely low material overlap, 0.05, with proper lattice (a), post radius (r), and airguide width (d). Model shows good design tolerance to achieve loss $\ll 0.1 \text{ cm}^{-1}$.

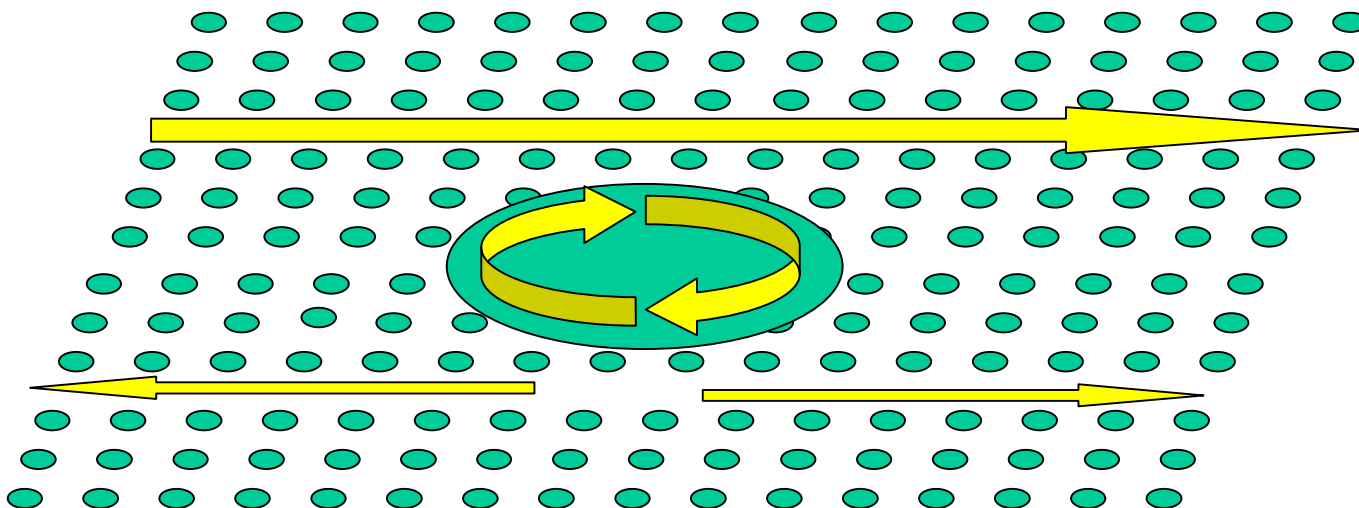
Development of Low-loss Waveguides Architectural Considerations (5)

3-D photonic crystal “airguide”



- Developed using InP/InGaAsP or InAlAs regrowth technology.
- No use of complex wafer bonding techniques.
- Forms full 3-D photonic “woodpile” structure for complete modal confinement

Resonant Defect Coupling for Delay Taps



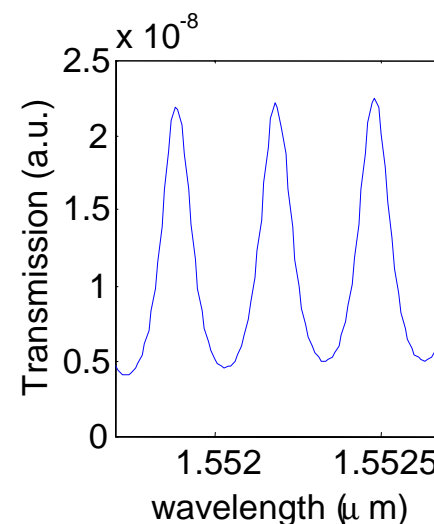
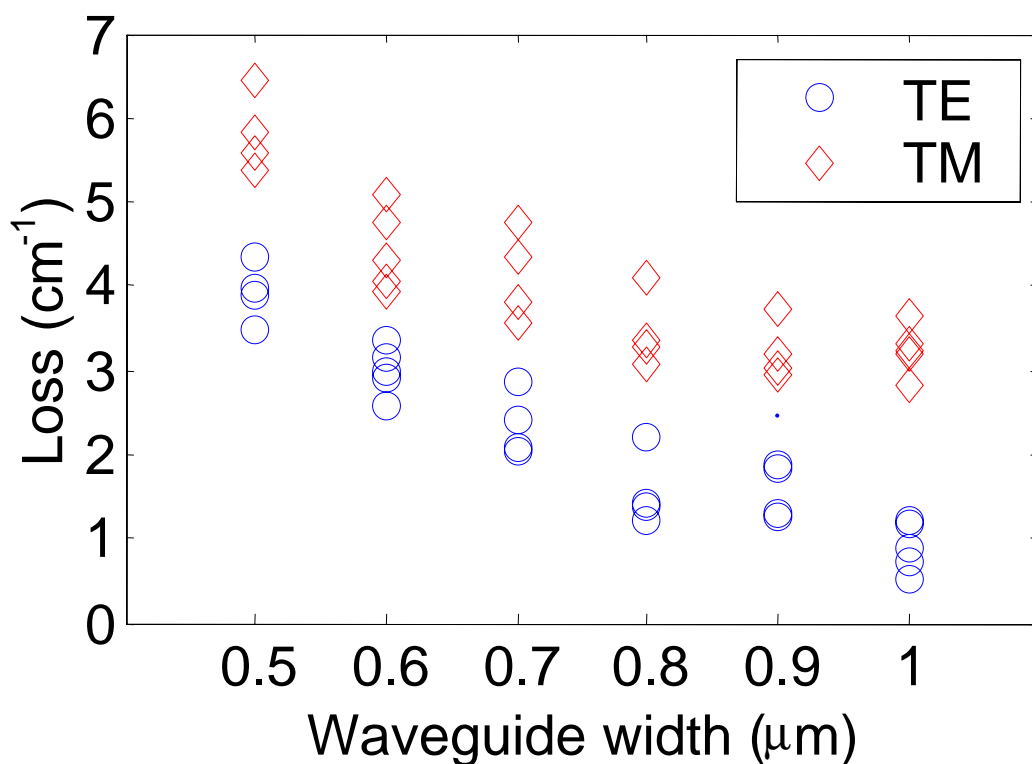
Resonant coupler acts as photonic crystal defect. Coupling can be tuned by use of the electro-optic effect.

Technical Challenges

- **Reduction of sidewall roughness in low-dimensional photonic structures.**
- **Vertical mode confinement in 1-D and 2-D waveguide structures.**
- **Tunability of resonant taps.**
- **Testing of sub-micron structures.**
- **Uniformity of resonant taps across 1 cm² sample.**

Preliminary Accomplishments

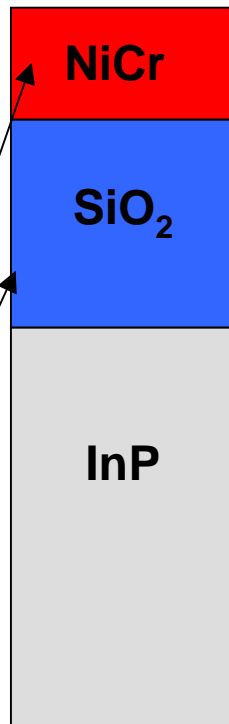
World record low-loss InP-waveguides



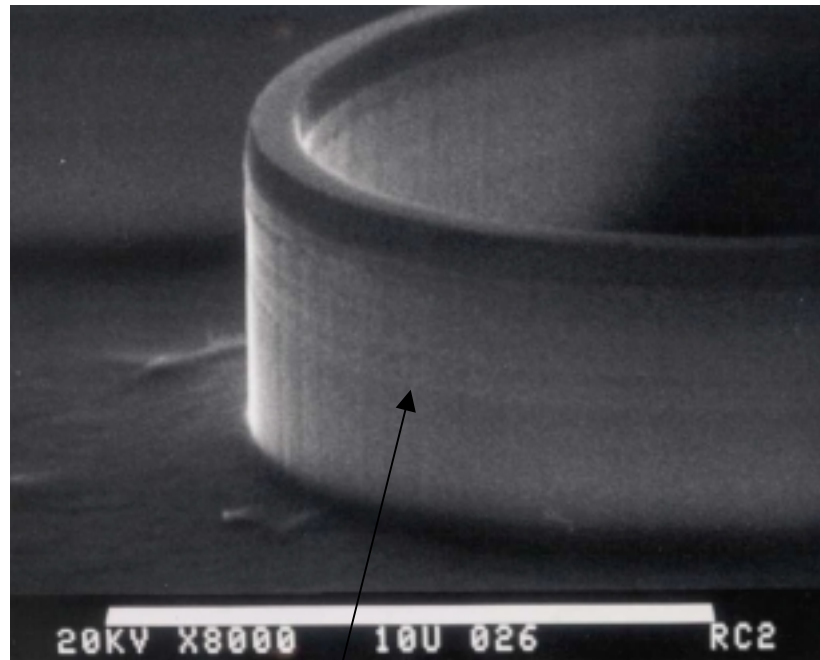
4 cm⁻¹ for 0.5 μm width x 4 μm depth waveguide!

Preliminary Accomplishments(2)

Ultra-smooth, high verticality etching of InP-based ring resonators.



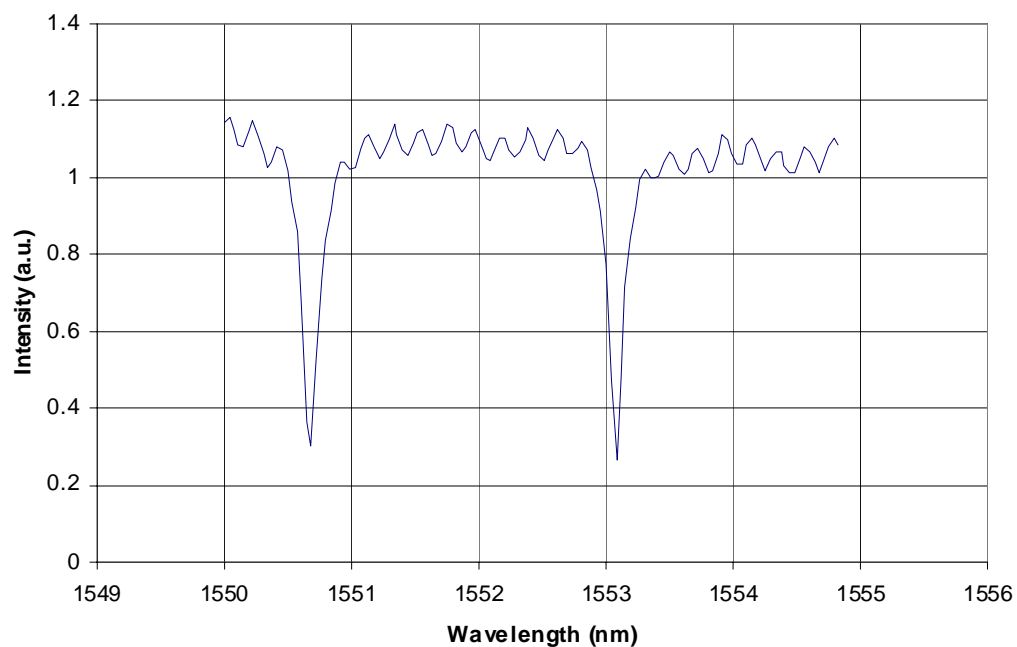
Bilayer mask to prevent erosion and reduce grain size.



Estimated rms sidewall roughness $\lambda/20$, achieved by Cl₂/H₂/Ar ICP-RIE. Developed under DARPA-RFLICS program.

Preliminary Accomplishments(3)

High Q ring resonators

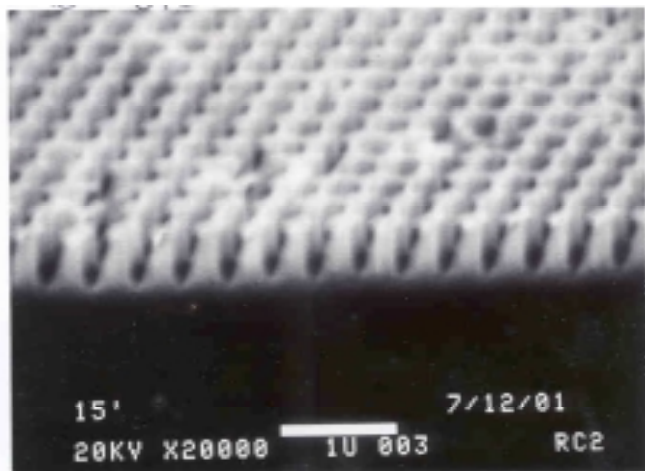


Achieved $Q > 10^4$ on DARPA RFLICS Program

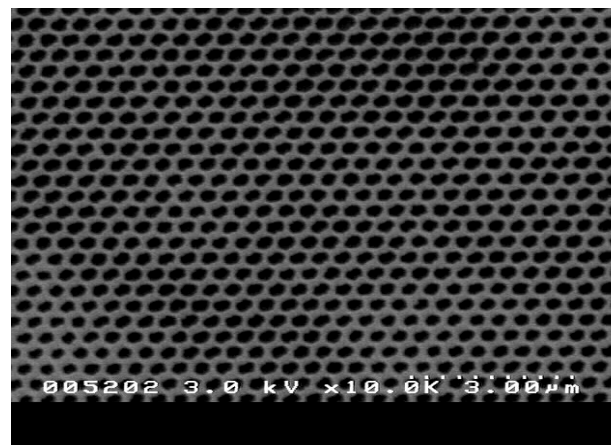


Preliminary Accomplishments(4)

Development of InP-based 2-D photonic crystal structures



Formation of InP “airguide” structure by dual holographic exposure and CH₄-based RIE.

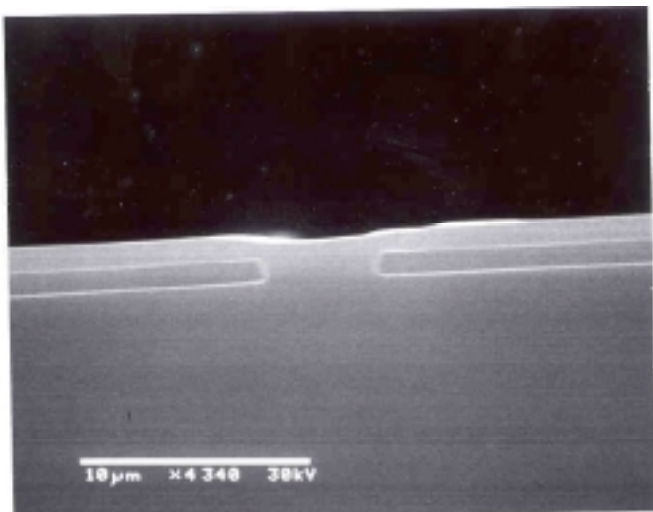


Formation of InP triangular hole lattice by direct write e-beam lithography and CH₄-based RIE.

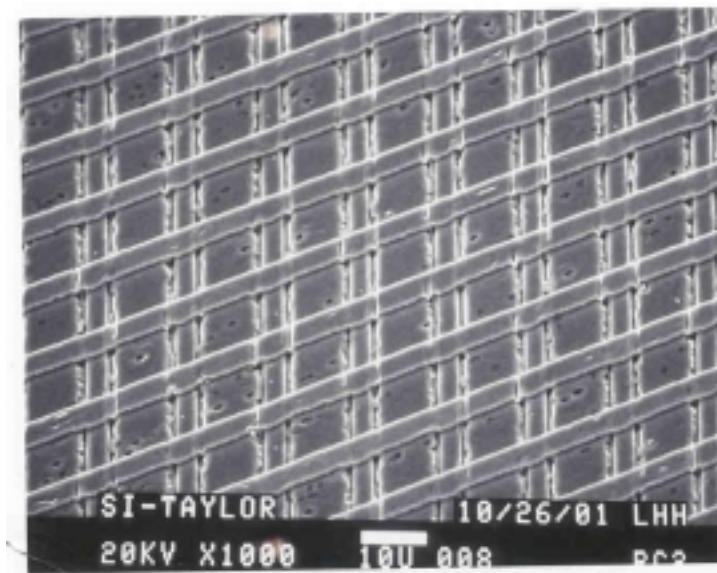
- 0.6 μm deep InP holes
- 0.15 μm radius
- 0.45 μm lattice spacing

Preliminary Accomplishments(5)

Development of 3-D InP “woodpile” structure



Establishment of InP/InAlAs regrowth to form base layers of 3-D woodpile.



Construction of InAlAs/InP layers.

Year 1 Activity

- **Development of low-loss InP-based waveguides based on rib or photonic crystal structures.**
- **Development of photonic crystal structure models and waveguide design for optimum reduction in loss.**
- **Design and fabrication of high-Q ($>10^6$) microring resonators.**
- **Fabrication of high-Q resonant filter arrays.**
- **Demo of high-Q filter arrays.**

SWAODL Program Plan

SWAODL	2002					2003					2004					2005					2006								
	J	J	A	S	O	N	D	J	F	M	A	M	J	J	A	S	O	N	D	J	F	M	A	M	J	J	A	S	
Task 1 Subwavelength Materials																													
1.1 Low-Loss Semiconductor Guides																													
1.2 3D Photonic Crystal (Woodpile)																													
1.3 Ultra-low-loss semiconductor materials																													
1.4 Low-loss 3D photonic crystal airguides																													
Task 2 Subwavelength Device Design																													
2.1 Low-loss material design																													
2.2 Photonic crystal model and filter design																													
2.3 Delay line and ultra-low loss subwavelength design																													
2.4 Integrated delay line design (modulator/laser)																													
Task 3 Filter Fabrication & Test																													
3.1 High-Q resonant filter arrays fabrication																													
3.2 Photonic crystal resonant filter arrays fab																													
3.3 Tapped delay line fabrication																													
3.4 Integrated delay line fabrication																													
Task 4 Component Demo																													
4.1 High-Q resonant filter array demo																													
4.2 Ultra high Q resonant filter array delivery to DARPA																													
4.3 Tapped delay line delivery to DARPA																													
4.4 Integrated tapped delay line deliver to DARPA																													



MT0 AOSP

APPROVED FOR PUBLIC RELEASE – DISTRIBUTION UNLIMITED

Sarnoff Sub-Wavelength Analog Optical Delay Lines